



CUSTOMER ADVISORY

Titanium and Topaz Development Kit Pre-Loaded Design Update

Advisory Details	<p>Titanium and Topaz development kits are shipped with HVIO banks (VCCIO pins) that are set to 3.3 V in hardware on the board; however, the bitstream that is pre-loaded into the flash device has a VCCIO bank setting of 1.8 V.</p> <p>Customers are strongly encouraged to download the latest development kit bitstream file from the Efinix Support Center, and re-program the board’s SPI flash device to avoid unexpected I/O behaviour due to incorrect VCCIO settings.</p> <p>Customers can refer to the following collateral for details:</p> <p>Development Kit User Guides:</p> <ul style="list-style-type: none"> • Titanium Ti375 N1156 Development Kit User Guide • Titanium Ti60 F225 Development Kit User Guide • Titanium Ti180 J484 Development Kit User Guide • Topaz Tz170 J484 Development Kit User Guide <p>Example Designs:</p> <ul style="list-style-type: none"> • Ti375 N1156 Development Kit PCIe Reads/Writes on Linux Demonstration Design • Ti60 F225 Development Kit Demonstration Design • Ti180 J484 Development Kit Demonstration Design • Tz170 J484 Development Kit Demonstration Design <p>Interfaces User Guides:</p> <ul style="list-style-type: none"> • Titanium Interface User Guide • Topaz Interface User Guide
Parts Affected	<p>Titanium and Topaz development kit ordering codes shown below:</p> <ul style="list-style-type: none"> • Ti375N1156C-DK • Ti60F225C-DK • Ti180J484C-DK • Tz170J484C-DK
Contact:	<p>For more information, please contact your local Salesperson or FAE, or contact us online at https://www.efinixinc.com/company-contact-us.html.</p>